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	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>ed</u> Q.A. <u>TM</u>	PATENT DATE
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APPLICATION NO. 09/996758	CONT/PRIOR F	CLASS 438	SUBCLASS 200 ⁺	ART UNIT 2812	EXAMINER H. PHAM
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Tadashi Oshima.

Semiconductor device with two types of FET's having different gate lengths and its manufacture method

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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